L Number	Hits	Search Text	DB	Time stamp
-	87	714/9.ccls.	USPAT;	2004/01/31
			US-PGPUB;	13:34
			EPO; JPO;	
			DERWENT;	
			IBM_TDB	
-	9	714/9.ccls. and (system adj controller)	USPAT;	2004/01/31
			US-PGPUB;	13:39
			EPO; JPO;	
			DERWENT;	
			IBM_TDB	
	3234	direct adj memory adj access adj controller	USPAT;	2004/01/31
			US-PGPUB;	13:40
			EPO; JPO;	
			DERWENT;	
			IBM_TDB	
•	8466	"dma" adj controller	USPAT;	2004/01/31
			US-PGPUB;	13:40
			EPO; JPO;	
			DERWENT;	
			IBM_TDB	
_	1091	(direct adj memory adj access adj	USPAT;	2004/01/31
		controller) and (error errors fault faults)	US-PGPUB;	13:40
		Controller, and Correct create launt launts,	EPO; JPO;	15115
			DERWENT;	
			IBM_TDB	
_	3656	("dma" adj controller) and (error errors fault	USPAT;	2004/01/31
_	3000	faults)	US-PGPUB;	13:40
		iduitoj	EPO; JPO;	10140
			DERWENT;	
			IBM_TDB	
_	1006	((direct adj memory adj access adj	USPAT;	2004/01/31
-	1000	controller) and (error errors fault faults))	US-PGPUB;	
		and (dual two multiple)	EPO; JPO;	13.41
		and (dual two multiple)	DERWENT;	
			I	
	680	(Ildus II adi aantuullan) naare (dual torra	IBM_TDB	2004/04/34
-	680	("dma" adj controller) near6 (dual two	USPAT;	2004/01/31
	1	multiple)	US-PGPUB;	13:41
			EPO; JPO;	
			DERWENT;	
		4.12	IBM_TDB	2004/04/04
-	135	(direct adj memory adj access adj	USPAT;	2004/01/31
		controller) near6 (dual two multiple)	US-PGPUB;	14:02
			EPO; JPO;	
			DERWENT;	
			IBM_TDB	
-	5	((direct adj memory adj access adj	USPAT;	2004/01/31
		controller) near6 (dual two multiple)) and	US-PGPUB;	14:02
		714/\$.ccls.	EPO; JPO;	
			DERWENT;	
	1		IBM_TDB	

- 482 "pci" adj controller USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM			•		
Po; JPO; DERWENT; IBM_TDB USPAT; US-PGPUB; EPO; JPO; DERWENT; IB	-	482	"pci" adj controller	7	
- 16				· · · · · · · · · · · · · · · · · · ·	14:41
- 16				1 '	
- 16 ("pci" adj controller) near6 (dual multiple two) - 18 peripheral adj component adj interconnect adj controller - 18 peripheral adj component adj interconnect adj controller - 272 ("pci" adj controller) same ("pci" adj bus) - 272 ("pci" adj controller) same ("pci" adj bus) - 367 ("pci" adj controller) and ("pci" adj bus) - 367 ("pci" adj controller) and ("pci" adj bus) - 151 (("pci" adj controller) and ("pci" adj bus) - 151 (("pci" adj controller) and ("pci" adj bus)) - 151 (("pci" adj controller) and ("pci" adj bus)) - 151 (("pci" adj controller) and ("pci" adj bus)) - 151 (1502 ("pci" adj controller) - 151 (1502 ("pci" adj controller) - 151 (1502 ("pci" adj controller) - 151 ("pci" adj controller) - 152 ("pci" adj controller) - 153 ("pci" adj controller) and ("pci" adj bus)) - 154 ("pci" adj controller) and ("pci" adj bus)) - 155 ("pci" adj controller) - 156 ("pci" adj controller) and ("pci" adj bus)) - 157 ("pci" adj controller) and ("pci" adj bus)) - 158 ("pci" adj controller) and ("pci" adj bus)) - 150 ("pci" adj controller) and ("pci" adj bus)) - 151 ("pci" adj controller) and ("pci" adj bus)) - 152 ("pci" adj controller) and ("pci" adj bus)) - 154 ("pci" adj controller) and ("pci" adj bus)) - 156 ("pci" adj controller) and ("pci" adj bus)) - 157 ("pci" adj controller) and ("pci" adj bus)) - 158 ("pci popula; - 1604/01/31 - 16:02 - 1604/01/31 - 16:02 - 174/11.ccls. and (system adj controller)				DERWENT;	
two) two) 18				_	
- 18 peripheral adj component adj interconnect adj controller	-	16	("pci" adj controller) near6 (dual multiple		2004/01/31
- 18			two)	US-PGPUB;	14:03
18				EPO; JPO;	
- 18 peripheral adj component adj interconnect adj controller US-PGPUB; EPO; JPO; DERWENT; IBM_TDB USPAT; US-PGPUB; EPO; JPO; DERWENT; ITILIZE EPO; JPO;				DERWENT;	
adj controller SPGPUB; EPG; JPO; DERWENT; IBM_TDB USPAT; US-PGPUB; EPO; JPO; DERWENT; IFO; JPO; JPO; DERWENT; IFO; JPO; JPO; JPO; JPO; JPO; JPO; JPO; JP				IBM_TDB	
- 272 ("pci" adj controller) same ("pci" adj bus) - 272 ("pci" adj controller) same ("pci" adj bus) - 367 ("pci" adj controller) and ("pci" adj bus) - 367 ("pci" adj controller) and ("pci" adj bus) - 151 (("pci" adj controller) and ("pci" adj bus)) and 710/\$.ccls. - 430 714/11.ccls. - 31 714/11.ccls. and (system adj controller) - 139 714/12.ccls. not 714/11.ccls. EPO; JPO; DERWENT; IBM_TDB USPAT; US-PGPUB; Ifo:02 USPAT; US-PGPUB	-	18	peripheral adj component adj interconnect	USPAT;	2004/01/31
- 272 ("pci" adj controller) same ("pci" adj bus) - 367 ("pci" adj controller) and ("pci" adj bus) - 367 ("pci" adj controller) and ("pci" adj bus) - 367 ("pci" adj controller) and ("pci" adj bus) - 151 (("pci" adj controller) and ("pci" adj bus)) - 151 (("pci" adj controller) and ("pci" adj bus)) - 151 (("pci" adj controller) and ("pci" adj bus)) - 2004/01/31 US-PGPUB; EPO; JPO; DERWENT; IBM_TDB USPAT; US-PGPUB; EPO; JPO; DERWENT;		1	adj controller	US-PGPUB;	14:26
- 272 ("pci" adj controller) same ("pci" adj bus) ("pci" adj controller) same ("pci" adj bus) - 367 ("pci" adj controller) and ("pci" adj bus) - 367 ("pci" adj controller) and ("pci" adj bus) - 151 (("pci" adj controller) and ("pci" adj bus))				EPO; JPO;	
- 272 ("pci" adj controller) same ("pci" adj bus) - 367 ("pci" adj controller) and ("pci" adj bus) - 367 ("pci" adj controller) and ("pci" adj bus) - 367 ("pci" adj controller) and ("pci" adj bus) - 151 (("pci" adj controller) and ("pci" adj bus))				DERWENT;	
US-PGPUB; EPO; JPO; DERWENT; IBM_TDB US-PGPUB; EPO; JPO; DERWENT; ISM_TDB US-PGPUB; EPO; JPO; DERWENT; INS-PGPUB;		1		IBM_TDB	
- 367 ("pci" adj controller) and ("pci" adj bus) - 367 ("pci" adj controller) and ("pci" adj bus) - 151 (("pci" adj controller) and ("pci" adj bus))	-	272	("pci" adj controller) same ("pci" adj bus)	USPAT;	2004/01/31
- 367 ("pci" adj controller) and ("pci" adj bus) - 367 ("pci" adj controller) and ("pci" adj bus) - 151 (("pci" adj controller) and ("pci" adj bus))]		US-PGPUB;	14:42
- 367 ("pci" adj controller) and ("pci" adj bus) - 151 (("pci" adj controller) and ("pci" adj bus)) - 151 (("pci" adj controller) and ("pci" adj bus)) - 151 (("pci" adj controller) and ("pci" adj bus)) - 151 (("pci" adj controller) and ("pci" adj bus)) - 151 (("pci" adj controller) and ("pci" adj bus)) - 151 (("pci" adj controller) and ("pci" adj bus)) - 151 (("pci" adj controller) and ("pci" adj bus)) - 151 (("pci" adj controller) and ("pci" adj bus)) - 151 (("pci" adj controller) and ("pci" adj bus)) - 151 (("pci" adj controller) and ("pci" adj bus)) - 151 (("pci" adj controller) and ("pci" adj bus)) - 151 (("pci" adj controller) and ("pci" adj bus)) - 152 ("pci adj controller) and ("pci" adj bus) - 153 ("pci" adj bus) - 154 ("pci" adj bus) - 155 ("pci" adj bus) - 156 ("pci" adj				EPO; JPO;	
- 367 ("pci" adj controller) and ("pci" adj bus) - 151 (("pci" adj controller) and ("pci" adj bus)) - 151 (("pci" adj controller) and ("pci" adj bus)) - 151 (("pci" adj controller) and ("pci" adj bus)) - 151 (("pci" adj controller) and ("pci" adj bus)) - 151 (("pci" adj controller) and ("pci" adj bus)) - 151 (("pci" adj controller) and ("pci" adj bus)) - 151 (("pci" adj controller) and ("pci" adj bus)) - 151 (("pci" adj controller) and ("pci" adj bus)) - 151 (("pci" adj controller) and ("pci" adj bus) - 151 (("pci" adj bus) - 151 (("pci" adj bus) - 151 (("pci" adj bus) - 152 (2004/01/31 - 14:45 - 1				DERWENT;	
US-PGPUB; EPO; JPO; DERWENT; IBM_TDB USPAT; US-PGPUB; EPO; JPO; DERWENT; EPO; JPO; DERWENT;				IBM_TDB	
- 151 (("pci" adj controller) and ("pci" adj bus)) and 710/\$.ccls. - 430 714/11.ccls. - 31 714/11.ccls. and (system adj controller) - 139 714/12.ccls. not 714/11.ccls. EPO; JPO; DERWENT; IBM_TDB USPAT; US-PGPUB; EPO; JPO; DERWENT; ISM_TDB USPAT; US-PGPUB; EPO; JPO; DERWENT; EPO; JPO; DERWENT; EPO; JPO; DERWENT;	-	367	("pci" adj controller) and ("pci" adj bus)	USPAT;	2004/01/31
- 151 (("pci" adj controller) and ("pci" adj bus)) and 710/\$.ccls. - 430 714/11.ccls. - 31 714/11.ccls. and (system adj controller) - 139 714/12.ccls. not 714/11.ccls. DERWENT; IBM_TDB USPAT; US-PGPUB; EPO; JPO; DERWENT; ISM_TDB USPAT; US-PGPUB; EPO; JPO; DERWENT; ISM_TDB USPAT; US-PGPUB; EPO; JPO; DERWENT; ISM_TDB USPAT; US-PGPUB; IN-PGPUB; IN-PGPUB				US-PGPUB;	14:45
- 151 (("pci" adj controller) and ("pci" adj bus)) and 710/\$.ccls. - 430 714/11.ccls. - 31 714/11.ccls. and (system adj controller) - 139 714/12.ccls. not 714/11.ccls. DERWENT; IBM_TDB USPAT; US-PGPUB; EPO; JPO; DERWENT; ISM_TDB USPAT; US-PGPUB; EPO; JPO; DERWENT; ISM_TDB USPAT; US-PGPUB; EPO; JPO; DERWENT; ISM_TDB USPAT; US-PGPUB; IN-PGPUB; IN-PGPUB				EPO; JPO;	
- 151 (("pci" adj controller) and ("pci" adj bus)) and 710/\$.ccls. - 430 714/11.ccls. - 31 714/11.ccls. and (system adj controller) - 139 714/12.ccls. not 714/11.ccls. IBM_TDB USPAT; US-PGPUB; EPO; JPO; DERWENT; US-PGPUB;		İ			
- 151 (("pci" adj controller) and ("pci" adj bus)) and 710/\$.ccls. - 430 714/11.ccls. - 31 714/11.ccls. and (system adj controller) - 139 714/12.ccls. not 714/11.ccls. USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB USPAT; US-PGPUB; EPO; JPO; DERWENT; ISM_TDB USPAT; US-PGPUB; EPO; JPO; DERWENT; ISM_TDB USPAT; US-PGPUB; EPO; JPO; DERWENT;				· ·	
and 710/\$.ccls. US-PGPUB; EPO; JPO; DERWENT; IBM_TDB USPAT; US-PGPUB; EPO; JPO; DERWENT; US-PGPUB; EPO; JPO; DERWENT; US-PGPUB; EPO; JPO; DERWENT;	-	151	(("pci" adj controller) and ("pci" adj bus))	_	2004/01/31
EPO; JPO; DERWENT; IBM_TDB USPAT; US-PGPUB; EPO; JPO; DERWENT; US-PGPUB; EPO; JPO; DERWENT; US-PGPUB; EPO; JPO; DERWENT;				· · · · · · · · · · · · · · · · · · ·	14:45
- 430 714/11.ccls. DERWENT; IBM_TDB USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB USPAT; US-PGPUB; EPO; JPO; DERWENT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB USPAT; US-PGPUB; EPO; JPO; DERWENT; US-PGPUB; EPO; JPO; DERWENT; US-PGPUB; EPO; JPO; DERWENT; US-PGPUB; EPO; JPO; DERWENT;				•	
- 430 714/11.ccls. IBM_TDB USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB USPAT; US-PGPUB; EPO; JPO; DERWENT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB USPAT;				' '	
- 430 714/11.ccls. USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB USPAT; US-PGPUB; EPO; JPO; DERWENT; US-PGPUB; EPO; JPO; DERWENT; US-PGPUB; EPO; JPO; DERWENT;					
- 31 714/11.ccls. and (system adj controller) US-PGPUB; EPO; JPO; DERWENT; IBM_TDB US-PGPUB; EPO; JPO; DERWENT; IBM_TDB 16:02 - 139 714/12.ccls. not 714/11.ccls. US-PGPUB; EPO; JPO; DERWENT; US-PGPUB; EPO; JPO; DERWENT; US-PGPUB; EPO; JPO; DERWENT;	_	430	714/11.ccls.		2004/01/31
- 31 714/11.ccls. and (system adj controller)				,	16:02
DERWENT; IBM_TDB USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB USPAT; US-PGPUB; EPO; JPO; DERWENT; US-PGPUB; EPO; JPO; DERWENT;		1		1	
- 31 714/11.ccls. and (system adj controller)					
- 31 714/11.ccls. and (system adj controller) USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB - 139 714/12.ccls. not 714/11.ccls. USPAT; US-PGPUB; EPO; JPO; DERWENT; EPO; JPO; DERWENT;					
US-PGPUB; EPO; JPO; DERWENT; IBM_TDB - 139 714/12.ccls. not 714/11.ccls. USPAT; US-PGPUB; EPO; JPO; DERWENT; EPO; JPO; DERWENT;	-	31	714/11.ccls. and (system adj controller)	_	2004/01/31
EPO; JPO; DERWENT; IBM_TDB - 139 714/12.ccls. not 714/11.ccls. USPAT; US-PGPUB; EPO; JPO; DERWENT;			,	*	16:02
DERWENT; IBM_TDB - 139 714/12.ccls. not 714/11.ccls. USPAT; US-PGPUB; EPO; JPO; DERWENT;					
- 139 714/12.ccls. not 714/11.ccls. IBM_TDB USPAT; 2004/01/31 US-PGPUB; EPO; JPO; DERWENT;					
- 139 714/12.ccls. not 714/11.ccls. USPAT; 2004/01/31 US-PGPUB; EPO; JPO; DERWENT;	:			· ·	
US-PGPUB; 17:22 EPO; JPO; DERWENT;	-	139	714/12.ccls. not 714/11.ccls.	USPAT;	2004/01/31
EPO; JPO; DERWENT;				*	17:22
DERWENT;				1	
				1 '	
,				IBM_TDB	
- 237 714/13.ccls. not 714/11,12.ccls. USPAT; 2004/01/31	-	237	714/13.ccls. not 714/11,12.ccls.	_	2004/01/31
US-PGPUB; 18:02			·	1	18:02
EPO; JPO;]			· ·	
DERWENT;					
		1		IBM_TDB	

-	630	714/48.ccls.	USPAT;	2004/01/31
			US-PGPUB;	19:05
			EPO; JPO;	
			DERWENT;	
			IBM_TDB	
-	68	address adj arbiter	USPAT;	2004/01/31
			US-PGPUB;	18:39
			EPO; JPO;	
			DERWENT;	
			IBM_TDB	
-	3894	(error) near6 interrupt	USPAT;	2004/01/31
			US-PGPUB;	18:39
			EPO; JPO;	
			DERWENT;	
			IBM_TDB	
•	63	(error adj message) near6 interrupt	USPAT;	2004/02/02
	E		US-PGPUB;	12:12
			EPO; JPO;	
			DERWENT;	
			IBM_TDB	